

Firmware Release Notes - FL SWITCH 3000 – 4000 - 4800 Series

1. Firmware Version: 1.xx / 2.xx

NOTE: Each firmware change will require more firmware images due to the introduction of new higher capacity hardware platforms (memory size, responsiveness).

Switch Type	Firmware File
• General FL SWITCH 3000 and 40xx	FL_SWITCH_3000_4000_v1_xx.bin
• FL SWITCH 4800E Rack Mount	FL_SWITCH_4800_v1_xx.bin
• POE - FL SWITCH 4004T-xxxxxx	FL_SWITCH_4004T_v2_xx.bin
• POE - FL SWITCH 4000T-xxxxxx	FL_SWITCH_4000T_v2_xx.bin

MIB archive	
FL SWITCH 3000 - 4000	FL_SWITCH_3000_4000_MIBs_v1_xx.zip
FL SWITCH 4800	FL_SWITCH_4800_MIBs_v1_xx.zip
FL SWITCH 4000T-xPOE-xSFP	FL_SWITCH_4000T_MIBs_v2_xx.zip
FL SWITCH 4004T-xPOE-xSFP	FL_SWITCH_4004T_MIBs_v2_xx.zip

2. Supported Hardware

The new firmware is available for all products below.

FL SWITCH 3000 – 4000 Series uses FL_SWITCH_3000_4000_v1_xx.bin

Order name	Order no
FL SWITCH 3005	2891030
FL SWITCH 3008	2891031
FL SWITCH 3005T	2891032
FL SWITCH 3004T-FX	2891033
FL SWITCH 3004T-FX ST	2891034
FL SWITCH 3008T	2891035
FL SWITCH 3006T-2FX	2891036
FL SWITCH 3006T-2FX ST	2891037
FL SWITCH 3012E-2FX	2891120
FL SWITCH 3012E-2FX SM	2891119
FL SWITCH 3012E-2SFX	2891067
FL SWITCH 3016	2891058
FL SWITCH 3016T	2891059
FL SWITCH 3016E	2891066
FL SWITCH 3006T-2FX SM	2891060
FL SWITCH 4008T-2SFP	2891062
FL SWITCH 4008T-2GT-4FX SM	2891061
FL SWITCH 4008T-2GT-3FX SM	2891160
FL SWITCH 4012T-2GT-2FX	2891063
FL SWITCH 4012T-2GT-2FX ST	2891161

FL SWITCH 4800 Series uses FL_SWITCH_4800_v1_xx.bin

Order name	Order no
FL SWITCH 4808E-16FX-4GC	2891079
FL SWITCH 4808E-16FX LC-4GC	2891073
FL SWITCH 4808E-16FX SM-4GC	2891080
FL SWITCH 4808E-16FX SM LC-4GC	2891074
FL SWITCH 4808E-16FX ST-4GC	2891085
FL SWITCH 4808E-16FX SM ST-4GC	2891086
FL SWITCH 4824E-4GC	2891072
FL SWITCH 4800E-24FX-4GC	2891102
FL SWITCH 4800E-24FX SM-4GC	2891104

FL SWITCH 4000T PoE Series uses FL_SWITCH_4000T_v2_xx.bin

Order name	Order no
FL SWITCH 4000T-4POE-SFP	1026924
FL SWITCH 4000T-8POE-2SFP	1026923

FL SWITCH 4004T PoE Series uses FL_SWITCH_4004T_v2_xx.bin

Order name	Order no
FL SWITCH 4004T-8POE-4SFP	1026922

3. History

Firmware-Version	Functions	
<ul style="list-style-type: none"> • 1.35 • • (Dec / 10 / 2018) 	<ul style="list-style-type: none"> • Added Functions <table border="1" data-bbox="565 345 1484 454"> <tr> <td data-bbox="565 345 1484 417"> <ul style="list-style-type: none"> • Added security: Allow MAC-Based Security across multiple switch interface ports for same MAC address. • Added security: HTTPS key generation feature. </td> </tr> </table> <p data-bbox="565 460 1484 504"><i>Bug fixes</i></p> <ul style="list-style-type: none"> • Added security: Upgraded switch security library. See CVE 2017-3735 • Added security: Added extended login time-out feature for consecutive invalid username / password combinations. See CVE-2018-13990 • Added security: Removed common key. All keys are now unique. See CVE 2018-13991. • Added security: Web UI pop-up reminder to enable HTTPS or disable the Web agent when saving a non-HTTPS enabled configuration. See CVE 2018-13992 • Added security: Implemented Cross-Site Request Forgery protection. See CVE 2018-13993 • Added security: Increased web agent denial-of-service immunity. See CVE-2018-13994 	<ul style="list-style-type: none"> • Added security: Allow MAC-Based Security across multiple switch interface ports for same MAC address. • Added security: HTTPS key generation feature.
<ul style="list-style-type: none"> • Added security: Allow MAC-Based Security across multiple switch interface ports for same MAC address. • Added security: HTTPS key generation feature. 		
<p data-bbox="240 989 565 1033">1.34 / 2.00</p> <p data-bbox="240 1054 565 1098">(May / 01 / 2018)</p>	<p data-bbox="565 989 1484 1033"><i>Added Functions</i></p> <ul style="list-style-type: none"> • Supports Modbus/TCP protocol for switch diagnostics • Added security: Configuration files uploads available in secure binary format • Supports new PoE managed switches (2.xx only) <p data-bbox="565 1185 1484 1229"><i>Bug fixes</i></p> <ul style="list-style-type: none"> • Added security: Prevent stack buffer overflows in index.cgi and menu_pxc.cgi. See CVE-2018-10728 • Added security: Prevent stack buffer overflow in shared object file. See CVE-2018-10731 • Added security: Prevent direct object reference to read-only switch configuration file. See CVE-2018-10729 • Added security: Prevent authenticated remote code execution. See CVE-2018-10730 	

Firmware-Version	Functions
1.33 (Dec. / 04 / 2017)	<i>Added Functions</i>
	<ul style="list-style-type: none"> • Added security: HTTPS WebUI connection with TLS only • Added security: HTTPS WebUI connection with TLS 1.2 • Added security: Enable/Disable: Monitor Mode, which corrects the issue addressed in CVE-2017-16741 • Added security: Force Complex Admin/User Passwords • Added security: Web Server updated to latest version • Added security: Add Secure Web Headers
	<i>Bug fixes</i>
	<ul style="list-style-type: none"> • Added security: Allow HTTPS WebUI connection with updated TLS 1.2 – 2048-bit group (Needs 6 seconds initial web page load time) • Prevent access to any items via Web UI without requiring user or admin account access. See CVE-2017-16743
1.32 (Sept. / 02 / 2016)	<i>Added functions</i>
	<ul style="list-style-type: none"> • New Model Support • Added security: SSL updated to latest version
1.31 (Sept. / 02 / 2016)	<i>Added functions</i>
	<ul style="list-style-type: none"> • Added security: HTTPS WebUI connection with TLS – 2048 bit group (Needs 6 seconds initial web page load time) • Extended Ring fail overs are logged in the “Event Table” • SNTP Server functionality
	<i>Bug fixes</i>
	<ul style="list-style-type: none"> • Added Security: FREAK (Factoring RSA Export Keys) security update in the OpenSSL Library. Removed older ciphers in SSL and TLS • Maintained access for FL View to automatically detect the FL SWITCH 3k / 4k / 48xxE series • Improved display format of the System Date and System Time • SNTP time synchronization can be triggered via SNMP and all IP addressing as well as Poll interval settings are operational • Native VID “saving” issue fixed, when the port member is Untagged • Port Based VLAN can be displayed correctly using the Chrome Web Browser • “Extended Ring” – “Path Control” menu, the “Ring-coupling port of Primary path” setting is consistently maintained after rebooting the switch.

Firmware-Version	Functions
1.30 (Aug. / 31 / 2015)	<p><i>Added functions</i></p> <ul style="list-style-type: none"> Extended Ring – Path Control <p><i>Bug fixes</i></p> <ul style="list-style-type: none"> Improved VLAN setting (Native VLAN ID of the port can be modified, when port is "Tagged") When the QoS is globally enabled, the default "Trust mode" is 802.1p is also enabled Corrected potential for rebooting based on specific topology and specific configuration (LLDP – IGMP – GVRP - STP) SFP combo port (4800E's series) detects its neighbor via LLDP. Global Flow Control web setting also enables flow control on all ports (previously, both Global and individual ports web settings had to be made)
1.22 (Dec. / 08 / 2014)	<p><i>Added functions</i></p> <ul style="list-style-type: none"> NMS (Network Management Software) added LLDP support Flow control default setting is disabled globally Enhanced SysObjectID table with Phoenix Contact Ethernet devices Added Security: (OpenSSL / Heartbleed and TLS / POODLE Bites security update) <p><i>Bug fixes</i></p> <ul style="list-style-type: none"> Selectable SSL support (security update) Native VLAN configuration (PVID) independent from the port setting in the Static VLAN Configuration Improved MIB file to the FL SNMP-OPC Server v 3.20 Packets with inappropriate VLAN ID are rejected by the Switch IC Basic Serial Interface doesn't display unknown message when configure STP setting Admin path cost of STP Port Config Table page can be set to 0. flWorkNetPortModus of Giga port can display correct value when Giga port set to 1000/FD via web page.
1.21 (Jan. / 24 / 2014)	<p><i>Added functions</i></p> <ul style="list-style-type: none"> First FW release compatible with FL SWITCH 4800 switches (4800's require separate FW file) <p><i>Bug fixes</i></p> <ul style="list-style-type: none"> Ring operates when booting up in a linear topology Config file transfer is possible via SNMP Consistent indication of manual port setting via web UI Local Diagnostics page includes complete descriptions All Trap OID's are synchronized to the trap events Extended Ring failure sends a trap message Cold start time decreased

Firmware-Version	Functions
1.20 (Sept. / 13 / 2013)	<i>Added functions</i>
	<ul style="list-style-type: none"> • Dual Ring feature • Selectable direction to Port Mirroring feature • LLDP support in the private MIB
	<i>Bug fixes</i>
	<ul style="list-style-type: none"> • Modified the Port Mirroring web page for browser compatibility • BootP packet format is now consistent • After power up, the first event is correctly time stamped in the event table • Standard MIB2 sysName and the Phoenix Contact private MIB flWorkBasicName are now consistent • The default STP path cost of the aggregate port meets the latest IEEE specs • Both power supply status are accurately displayed • Isolate SFP setting change from temporarily impacting other SFP on 4008-2SFP switch • Allow a single port to be set as Ring Coupling via SNMP • NTP synchronization operates with the NTP Server • All Link aggregation configuration settings are operational • Can change back to "private" as a password.
1.10 (Jan. / 18 / 2013)	<i>Added functions</i>
	<ul style="list-style-type: none"> • Added LLDP feature
	<i>Bug fixes</i>
	<ul style="list-style-type: none"> • Long file names are allowed in the Config File Transfer and the Software Update menus. • MAC based security - Global Discard operates consistently • Increase RSTP topology change counter accuracy • GMRP and GVRP work over a link aggregation trunk. • BootP client operates consistently • RSTP port priority setting matches IEEE requirements
1.00 (March. / 30 / 2012)	<ul style="list-style-type: none"> • First Firmware release